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				APPLICA	APPLICANT: Lieber, et al.				
				GROUP	GROUP ART UNIT: 2813		EXAMINER: D.A. Wolverton		
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Initials #	No	(book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, and/or country where published.						(Y/N)	
	FRIEDMAN, Robin S. et al. "High-speed integrated nanowire circuits" NATURE, Vol. 434, April 28, 2005, page 1085 IBM News, 2005, "IBM creates highest performing nanotube transistors" www.ibm.com/news/us/2002/05/20.html Office Action from Serial No. 11/543,337 dated March 18, 2008							!	
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# EXAMINER: Include copy of	Initial if ret	ference consider	red, whether or a	not citation is in licant.	conformance with MPEP 609; Draw lin	ne through citation i	f not in conformance and	not considered.	
*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No filed aupon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).									
are included. Se	e 37 CFR § aired by 37	1.98 and 12870 CFR §1.98 unl	10162 Camina a	f all other patent s were provided	pending, unpublished patent applicatior t(s), publication(s), unpublished, pendir in an IDS in an earlier application that	IZ II.S. DALEDE ADDIR	MURIES, OF OUICE INTOTHION	OII HISCOR DIO	